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SERIAL NUMBER 10/670,352	FILING DATE 09/26/2003 RULE	CLASS 438	GROUP ART UNIT 2812	ATTORNEY DOCKET NO. 501.41197CX1
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APPLICANTS

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** CONTINUING DATA *****
 This application is a CON of 10/096,801 03/14/2002 PAT 6,638,779
Yes

** FOREIGN APPLICATIONS *****
 JAPAN 2001-075671 03/16/2001
Yes

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Anto Stevenson</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 13	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 3
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TITLE
 Fabrication method of semiconductor integrated circuit device and testing method

FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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